



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

PATENT

Image
2816
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IN RE

APPLICATION OF: Joshi et al.

SERIAL NO.: 10/078,340

FILED: February 20, 2002

FOR: SOLDER INTERCONNECTIONS FOR
FLAT CIRCUITS

EXAMINER: A. Paladini

ART UNIT: 2125

CONFIRMATION NO.: 7950

DATE OF
NOTICE OF ALLOWANCE: December 18, 2003

ATTORNEY DOCKET NO.: A2-088 US

) I hereby certify that this document along
) with any documents referred to as being
) attached, is being deposited with the United
) States Postal Service on the date shown
) below as first class mail, postage prepaid, in
) an envelope addressed to: Mail Stop Issue
) Fee, Commissioner for Patents, P.O. Box
) 1450, Alexandria, Virginia 22313-1450.

February 13, 2004
Date

Kerri Richardson

Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

Dear Sir:

TRANSMITTAL OF FORMAL DRAWINGS

On December 18, 2003, the office issued a Notice of Allowability (Paper No. 4) in this application. The Examiner has objected to the drawings expressly, the margins are unacceptable and the lines, number & letters are not uniformly thick and well defined, clean, durable and black.

Applicants hereby transmit **four (4)** sheets of formal drawings containing FIGS. 1- 12 and incorporating the changes requested by the Examiner.

Respectfully submitted,

MOLEX INCORPORATED

Date:

2/13/2004

By:

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